Electronic Patent Application Fee Transmittal									
Application Number:	09683677								
Filing Date:	01-Feb-2002								
Title of Invention:	Method of switching external models in an automated system-on-chip integrated circuit design verification system								
First Named Inventor/Applicant Name:	Robert J. Devins								
Filer:	Jack P. Friedman/Kimberly Dwileski								
Attorney Docket Number:	BUR920010098								
Filed as Large Entity									
Utility Filing Fees									
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)				
Basic Filing:									
Pages:									
Claims:									
Miscellaneous-Filing:									
Petition:									
Patent-Appeals-and-Interference:									
Post-Allowance-and-Post-Issuance:									
Extension-of-Time:									

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)	
Miscellaneous:					
Request for continued examination	1801	1	790	790	
	Tota	Total in USD (\$)			